	APPROVAL SHEET	
MODELNO:	COG-T350KFY-05	
Approval	option:	
	□ Sample	
Customer	's Confirmation	
Custom	A A A A A A A A A A A A A A A A A A A	
Approve Date:	ed by:	
Note:	- A	
-	A STA	

Center Confirmed:

Approved	Checked by	Made by

REVISED SUMMARY DATE **REF. PAGE** REMARK PARAGRAPH No. DRAWING No. 2015-3-11 01 je. 10

Records of Revision

Contents



1. Introduction

1.1 Scope of application

This specification applies to the positive type TFT transmissive dot matrix LCD module.

LCD specification: Dots 320xRGBx240.

As to basic specification of the driver IC, refer to the IC (HX8238D) specification and datasheet.

1.2 Structure:

Module display structure: TFT Module + FPC +BL 16.7M FULL Dithering Color3.5 inch TFT LCD size for main LCD; One bare chip with gold bump (COG) TECH;

1.3 TFT features:

Structure: TFT PANNEL+IC+FPC+BL; Transmissive Type LCD 320 dot-source and 240 dot-gate outputs; White LED back light;

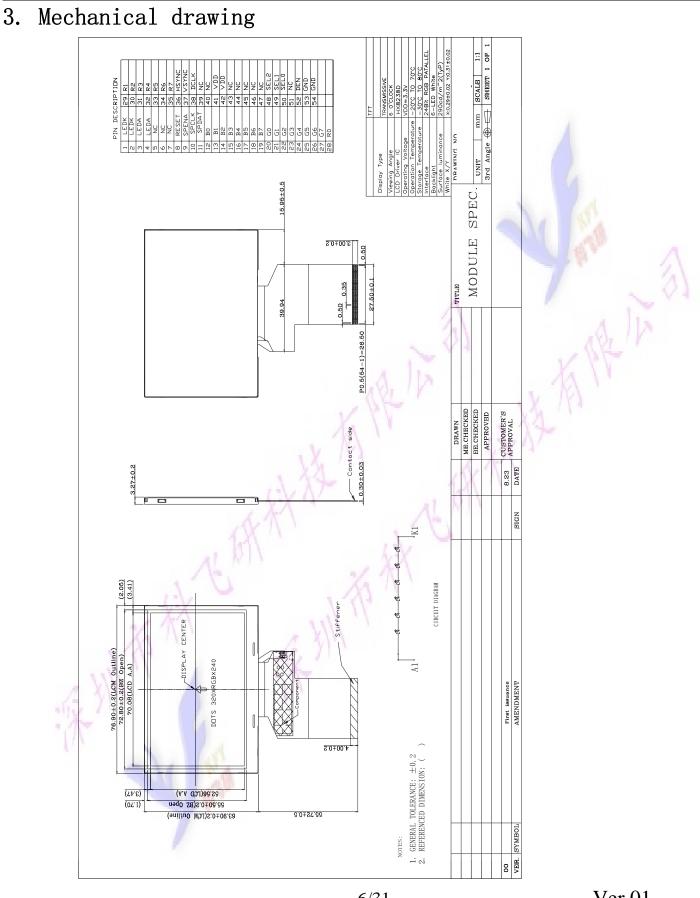
1.4 Applications:

Mobile phone

PSP PDA GPS Etc…

2. General specification

_		
ITEM	Standard value	UNIT
LCD Type	TFT Transmissive	
Driver element	a-Si TFT Active matrix	
Number of Dots	320*(RGB)*240	Dots
Pixel Arrangement	RGB Vertical Stripe	21
Active Area	70.08 *52.56	mm
Viewing Area (W*H)	1/2	mm
Viewing Direction	12 0' clock	e
Driver IC	HX8238D	
Module Size(W*H*T)	76. 9x63. 9x3. 26	mm
Approx. Weight	TBD	g
Back Light	White LED	
System interface	24-bit RGB interface	



4. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Supply voltage	V _{CI}	-0.3	4.0	v
Input voltage for logic	V _{DDIO}	-0.5	V _{cc} +0.3	v A
Supply current (One LED)	ILED		30	mA
Operating temperature	T _{OP}	-20	+70	°C
Storage temperature	T _{st}	-30	+80	- °C

5. ELECTRICAL CHARACTERISTICS

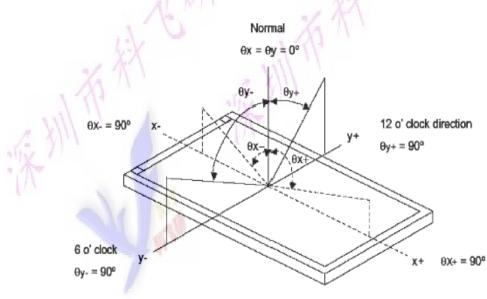
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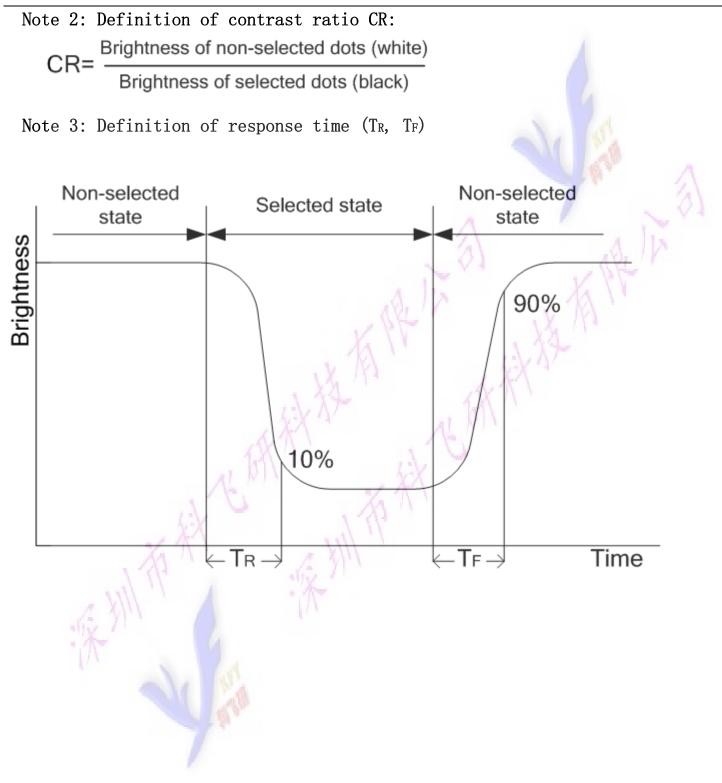
Item	Symbol	Min	Тур	Max	Unit	Applicable terminal
Supply voltage	V _{CI}	2.5	3.3	3.6	V	V_{DD}
Turnet 14 mil	V _{IL}	-0.3		0.2 V _{CI}	V	
Input voltage	V _{IH}	0.8 V _{CI}	K-X	V _{CI}	V	
Input leakage current	I_{LKG}	- %	XA	-	μA	
LED Forward voltage	V_{f}	3.0	3.2	3.4	V	With One LED
Input backlight current	I_{LED}	NIF .	20	25	mA	With One LED
LED life time	S.E.	20, 000			hr	

ТТЕМ		CVAIDOL	CONDITIONS	SPEC	IFICAT	IONS	UNITT	NOTE	
ITEM		SYMBOL	CONDITIONS	MIN.	TYP.	MAX	UNIT	NOTE	
Brightness		В		240	300		Cd/m^2		
Contrast Ra	tio	CR		300	400		/		
Response Ti	me	Tr+Tf			35		ms		
	Red	XR			0.633				
		Yr	Viewing		0.329			All left	
OTE	Green	XG	normal angle		0.297			side data	
CIE		YG			0.577			are based on	
Color coordinate	Blue	Хв			0.133			wasam's	
coordinate		Yв			0.129		1	product	
	White	Xw			0.294		VN	reference	
		Yw		1.12	0.334		121	only	
	Hor.	θ_{X^+}		50	60		M		
Viewing		$\theta_{_{X-}}$	Center	50	60	3-44	Deer		
Angle	Ver.	$\theta_{_{Y+}}$	CR>=10	40	50	KX-Y	Deg.		
		$ heta_{_{Y-}}$	1 12		55	N.			
Uniformity	Un		XX Y	80	85		%		

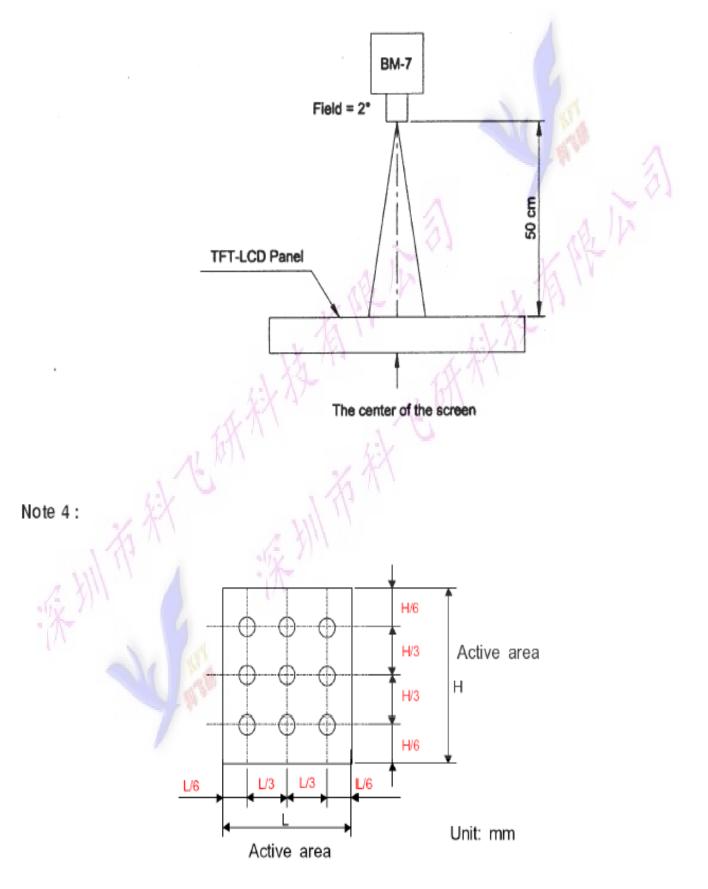
6. OPTICAL CHARACTERISTICS

Note 1 : Definition of Viewing Angle8 xand8 v:





The brightness test equipment setup 20mA Field=2° (As measuring "black" image, field=2° is the best testing condition)



7. MCU Interface Pin Function

Pin No.	Symbol	Description
1	LED	Backlight LED Ground
2	LED	Backlight LED Ground
3	LED+	Backlight LED Power
4	LED+	Backlight LED Power
5	NC	Not Use
6	NC	Not Use
7	NC	Not Use
8	/RESET	Hardware Reset
9	SPENA	SPI Interface Data Enable Signal
10	SPCLK	SPI Interface Data Clock
11	SPDAT	SPI Interface Data
12	В0	Blue Data Bit O
13	B1	Blue Data Bit 1
14	B2	Blue Data Bit 2
15	B3	Blue Data Bit 3
16	B4	Blue Data Bit 4
17	B5	Blue Data Bit 5
18	B6	Blue Data Bit 6
19	B7	Blue Data Bit 7
20	GO	Green Data BitO
21	G1	Green Data Bit1
22	G2	Green Data Bit2
23	G3	Green Data Bit3
24	G4	Green Data Bit4
25	G5	Green Data Bit5
26	G6	Green Data Bit6
27	G7	Green Data Bit7
28	RO	Red Data Bit0 /DX0
29	R1	Red Data Bit1 /DX1
30	R2	Red Data Bit2 /DX2
31	R3	Red Data Bit3 /DX3
32	R4	Red Data Bit4 /DX4
33	R5	Red Data Bit5 /DX5
34	R6	Red Data Bit6 /DX6

	Table	2:	Pin	assignment
--	-------	----	-----	------------

_				
	35	R7	Red Data Bit7 /DX7	
	36	HSYNC	Horizontal Sync Input	
	37	VSYNC	Vertical Sync Input	
	38	DCLK	Dot Data Clock	
	39	NC	Not Use	
	40	NC	Not Use	
	41	VCC	Digital Power]
	42	VCC	Digital Power	
	43	NC	Not Use	A
	44	NC	Not Use	31
	45	NC	Not Use	21
	46	NC	Not Use	
	47	NC	Internal test use]
	48	SEL2	Control the input data format /floating	
	49	SEL1	Control the input data format	
	50	SEL0	Control the input data format	1
	51	NC	Not Use]
	52	DE	Data Enable Input	
Ī	53	DGND	Ground	1
	54	AVSS	Ground	
			AL NI	

Note:

1. The mode control (SEL2) not use ,it can't control CCIR601 interface , If not use CCIR601 ,it

can floating.

2. For digital RGB input data format, both SYNC mode and DE+SYNC mode are supported. If $\mbox{\rm DE}$

signal is fixed low, SYNC mode is used. Otherwise, DE+SYNC mode is used.Suggest
used SYNC mode!!

Suggest the DE signal usually pull low.

3. usually pull high.

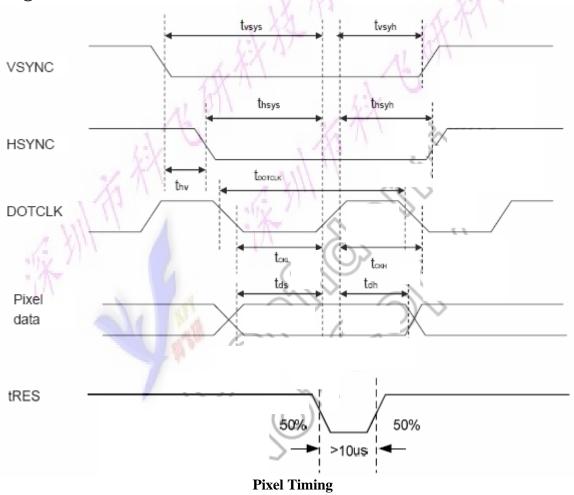
4. IF select serial RGB or CCIR601/656 input mode is selected, only DX0-DX7 used, and the other short to GND, 0nly

selected serial RGB, CCIR601/656 interface, DX BUS will enable, Digital input mode DXO is LSB and DX7 is MSB.

AIR

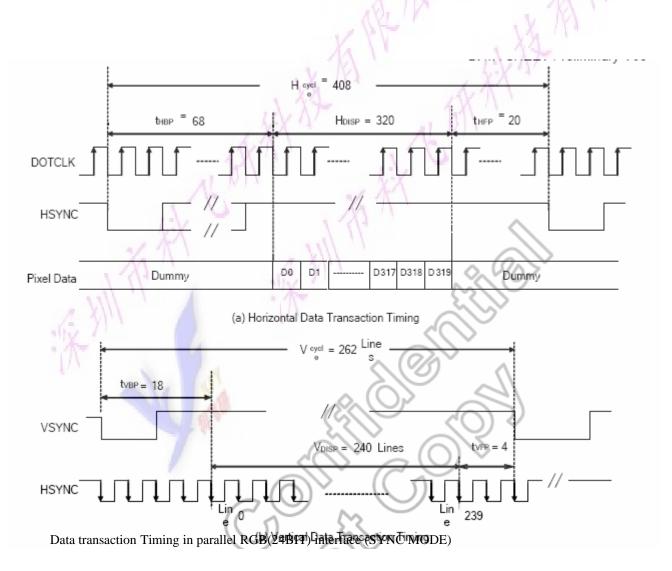
SEL1 SEL	0 Interface Mode Parallel-RGB Data format interface
0 0	Parallel-RGB Data format interface
	(only support stripe type color filter)
0 1	Serial-RGB data format
1 0	CCIR 656 data format (640RGB)
1 1	CCIR 656 data format (720RGB)
0 0	YUV mode A data format(Cr-Y-Cb-Y)
0 1	YUV mode A data format(Cr-Y-Cb-Y)
1 0	YUV mode B data format(Cb-Y-Cr-Y)
1 1	YUV mode B data format(Cb-Y-Cr-Y)
1 1 0	1 0 1 0 1 1





	(5)]							
Characteristics	Symbol	M	in.	Ту	/p.	Ma	ax.	Unit
characteristics	Symbol	24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	Onic
DOTCLK Frequency	fDOTCLK	-	-	6.5	19.5	10	30	MHz
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	-	-	ns
Vertical Sync Setup Time	tvsys	20	10	-	-	- /	-	ns
Vertical Sync Hold Time	tvsyh	20	10	-	-		4 -	ns
Horizontal Sync Setup Time	thsys	20	10	-	-		- 1	ns
Horizontal Sync Hold Time	thsyh	20	10	-	-	- / -	1	ns
Phase difference of Sync Signal Falling Edge	thv	1		-		24	40	tDOTCLK
DOTCLK Low Period	tCKL	50	15	-	-	-1/	-	ns
DOTCLK High Period	tCKH	50	15	-	-	- 1	-	ns
Data Setup Time	tds	12	10	- /	-	-	-	ns
Data hold Time	tdh	12	10	- 1	-	-		ns
Reset pulse width	tRES	1	0	1	-		1011	μs

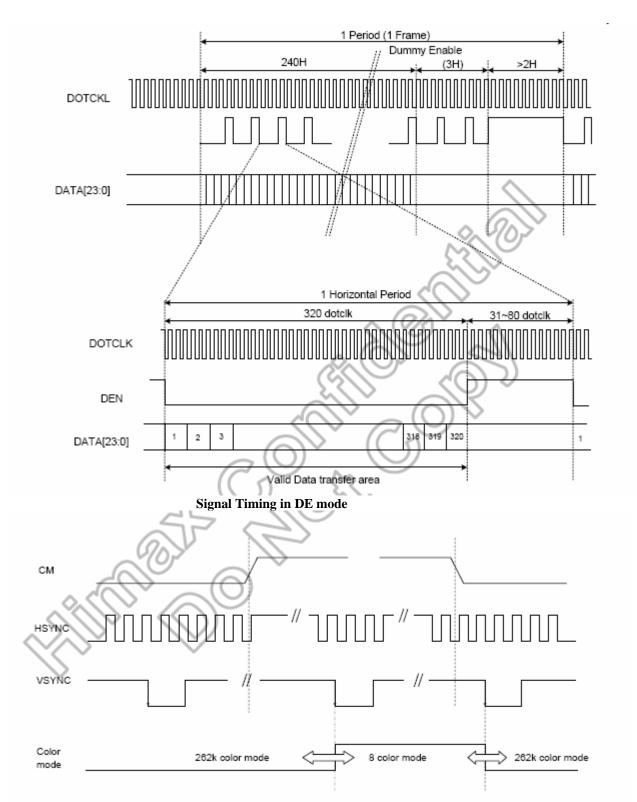
Note: External clock source must be provided to DOTCLK pin of HX8238-D. The driver will not operate if absent of the clocking signal.



			<u> </u>	_				-	_
Characteristics	Symbol	Min.		Ty	/p.	M	ax.	Unit	1
characteristics	Symbol	24 bit	8 bit	24 bit	8 bit	24 bit	8 bit		
DOTCLK Frequency	fDOTCLK		-	6.5	19.5	10	30	MHz	1
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	-	-	ns]
Horizontal Frequency (Lin	në) (H	-		14	1.9	22	.35	KHz]
Vertical Frequency (Refre		-		6	0	9	0	Hz	1
Horizontal Back Porch	tHBP	-	-	68	204	-	- 6	tDOTCLK]
Horizontal Front Porch	tHFP	-	-	20	60	- 2	-	tDOTCLK	1
Horizontal Data Start Poir	nt tHBP	-	-	68	204	- 1	- /	tDOTCLK	1
Horizontal Blanking Perio	d tHBP + tHFP	-	-	88	264	-	10-	tDOTCLK	1
Horizontal Display Area	HDISP	-	-	320	960	-	- 1	tDOTCLK	1
Horizontal Cycle	Hcycle	-	-	408	1224	450	1350	tDOTCLK	A
Vertical Back Porch	tVBP	-		1	8		/	Lines	44
Vertical Front Porch	tVFP	-		4	4		-	Lines	1 AC
Vertical Data Start Point	tVBP	-		1	8		- //	Lines	12
Vertical Blanking Period	tVBP + tVFP	-			2		-	Lines	
NTS					40			4 15	1
Vertical Display	VDISP	.			ALM=0)	1	-	Lines	
Area PAL					ALM=1)	-			
Vartical Quala NTS	<u> </u>						V	101-	1
Vertical Cycle PAL	Vcycle		-		262		50 /A	Lines	
				CH-		1	Y. 18	1	•
:			V				04 Y		
		H cyc	= 1224	11		1 9	1		
ſ		0				LV	$N \sim 1$		
		NX.			1	X-A-			
	нвр = 204	1. X.L	HDISP	= 960	- 24	thep _	60		
4			-						
fif∃ f	TE E	t f i f		- f - f	T f	f	- fi	f f f	-
DOTCLK	A.			1	14				
		μ \Box							
	11 0 1		,	, here'		~			
			_ //	1 Acres					
HSYNC	12. 20					\sim			
			X			(Ω)	<u> </u>		
					Y	~~(0)			
	41	1	<u></u>		$-\infty$	$\overline{}$			
Pixel Data	lummy	D0 D	1	D957 D9	58 D959	V I	Dummy		
		162 1							
11 /100					$\langle \langle \rangle \rangle$				
	(1) Hor	izontal Dat	ta Transa	ction Timir	Jo.				
12 111	(1).11			<u> (9</u>	3)	A			
217 V . 4				Line		~//~	.1		
* A		— V °	^{cl} = 262	6)2		21	*		
1/1			α	S.	6	\mathcal{D}			
tvbp = 1	8		151	5	- C($)) \checkmark$			
	14 N A	/	112	×		<			
	all all		XX		()		-		
VSYNC			\sim	AV	S				
VOTING		6	>	((1)					

VDISP = 240 Lines HSYNC Lin o Lin 239 е (2) Vertical Data Transaction Timing Data transaction Timing in serial RGB(8BIT) interface (SYNC MODE)

 $t_{VFP} = 4$



Note: The color mode conversion starts at the first falling edge of VSYNC after stage change of CM.

Color mode conversion Timing

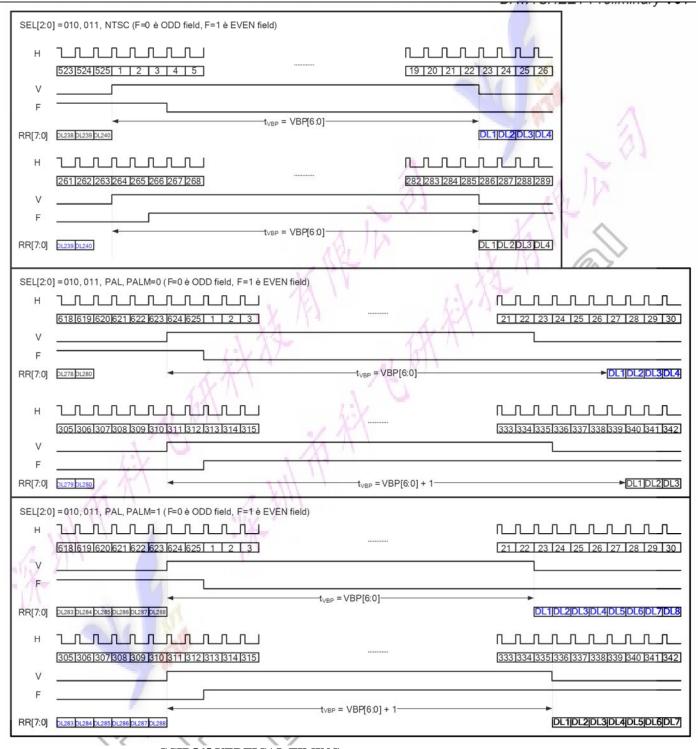
SEL[2:0] = 100, NTSC/PAL	
HSYNC	
RR[7:0] Invalid Data Cr1 Y1 Cb1 Y2 Cr320 Y639 Cb320 Y640 Invalid Data	
SEL[2:0] = 101, NTSC	
HSYNC	Λ
	201
RR[7:0] Invalid Data Cr1 Y1 Cb1 Y2 Cr360 Y719 Cb360 Y720 Invalid Data	Ita
← t _{HBP} = HBP[6:0]*4+STP[1:0] → ← H _{DISP} = 1440	f fasse
SEL[2:0] = 101, PAL	
нуумс	
RR[7:0] Invalid Data Cr1 Y1 Cb1 Y2 Cr360 Y719 Cb360 Y720 Invalid Data	
SEL[2:0] = 110, NTSC	
нуумс	
RR[7:0] Invalid Data Cb1 Y1 Cr1 Y2 Cb360 Y719 Cr360 Y720 Invalid Data	
SEL[2:0] = 110, PAL	
HSYNC	
RR[7.0] Invalid Data Cb1 Y1 Cr1 Y2 Cb360 Y719 Cr360 Y720 Invalid Data	
← t _{HBP} = HBP[6:0]*4+STP[1:0] H _{DISP} = 1440	
SEL[2:0] = 111, NTSC /PAL	
HSYNC	
RR[7:0] Invalid Data Cb1 Y1 Cr1 Y2 Cb320 Y639 Cr320 Y640 Invalid Data	

CCIR6011 HORIZONTAL TIMING

			COG-T350KFY-
SEL[2:0] = 100 ~ 111, NTS	с		1
EVEN Field		ODD Field	4
VSYNC	, L		
HSYNC			
	4 5 6 7	22 23 24 25	261 262 263
RR[7:0]	-t _{√BP} = VBP[6:0]	DL1 DL2 DL3	DL239DL240
			1 den
ODD Field-		-EVEN Field	
VSYNC			
264 265 266	267 268 269 270	285 286 287 288	524 525
RR[7:0]		→ DL1 DL2 DL3	DL239DL240
		A HA	44
SEL[2:0] = 100 ~ 111, PAL	, PALM=0	SK4 V.	
-EVEN Field		DD Field	-
	V KA	L'XYYY	
		תתהתתו	
1 2 3	4 5 6 7	26 27 28 29	305 306 307
RR[7:0]	-t _{vBP} = VBP[6:0]	→ DL1 DL2 DL3	DL279DL280
ODD	13X A	1.0	
Field VSYNC		VEN Field	
1	NI III		
RR[7:0]	$t_{VBP} = VBP[6:0] + 1$	DL1 DL2 DL3	DL279DL280
SEL[2:0] = 100 ~ 111, PAL	, PALM=1		
Field	oc	DD Field	
VSYNC			
			309 <u>310 311</u>
RR[7:0]	t _{vBP} = VBP[6:0]	DL1 DL2 DL3	DL287DL288
induol .	WEP - CE. [GIG]		
ODD	E`	VEN Field	
VSYNC			
313 314 315	<u>316 317 318 319</u>		
RR[7:0]		DL1 DL2 DL3	DL287DL288

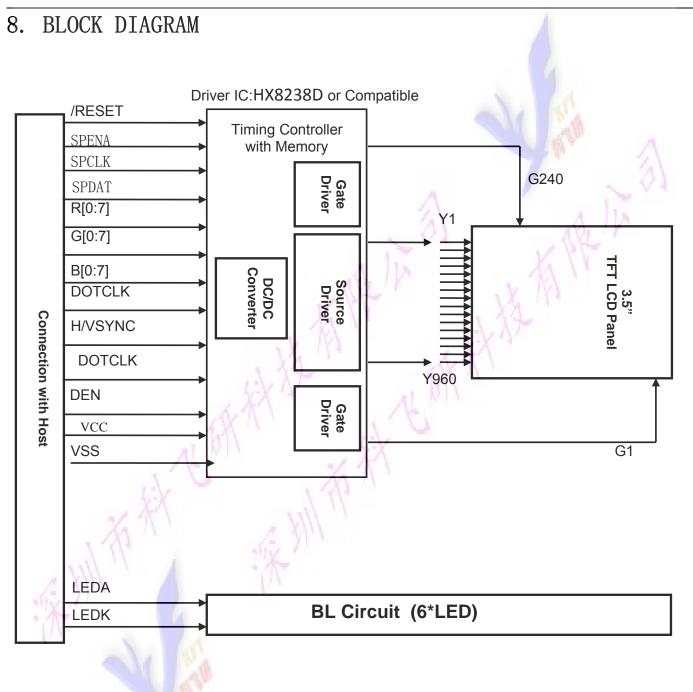
CCIR6011 VERTICAL TIMING





CCIR565 VERTICAL TIMING

HX8238D INITIAL CODE Set LCD 16B REG(0x0001, 0x6300); Set_LCD_16B_REG(0x0002, 0x0200); Set LCD 16B REG(0x0003, 0x7164); Set_LCD_16B_REG(0x0004, 0x044F); Set LCD 16B REG (0x0005, 0xB4D4); Set LCD 16B REG(0x000A, 0x4008); Set LCD 16B REG(0x000B, 0xD400); Set LCD 16B REG(0x000D, 0x123A); Set LCD 16B REG(0x000E, 0x2d00); Set LCD 16B REG(0x000F, 0x0000); Set_LCD_16B_REG(0x0016, 0x9F80); Set LCD 16B REG(0x0017, 0x2212); Set_LCD_16B_REG(0x001E, 0x00d0); Set LCD 16B REG(0x0030, 0x0507); Set LCD 16B REG(0x0031, 0x0004); Set_LCD_16B_REG(0x0032, 0x0707); Set_LCD_16B_REG(0x0033, 0x0000); Set LCD 16B REG(0x0034, 0x0000); Set_LCD_16B_REG (0x0035, 0x0307); Set LCD 16B REG (0x0036, 0x0700); Set LCD 16B REG(0x0037, 0x0000); Set LCD 16B REG(0x003A, 0x140B); Set LCD 16B REG(0x003B, 0x140B);



9. LCM Quality Criteria 9.1 VISUAL & FUNCTION INSPECTION STANDARD 9.1.1 Inspection conditions Inspection performed under the following conditions is recommended. Temperature : $25\pm5^{\circ}$ C $65\% \pm 10\%$ RH Humidity : Viewing Angle : Normal viewing Angle. Illumination: Single fluorescent lamp (300 to 700Lux) Viewing distance: 30-50cm Upper Polarizer Upper Glass Bottom glass **Bottom Polarizer** Light Source 9.1.2 Definition Zone A Zone B -Zone C Zone A : Effective Viewing Area(Character or Digit can be seen) Zone B : Viewing Area except Zone A Zone C : Outside (Zone A+Zone B) which can not be seen after assembly by customer .) Note: As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer.

9.1.3 Sampling Plan

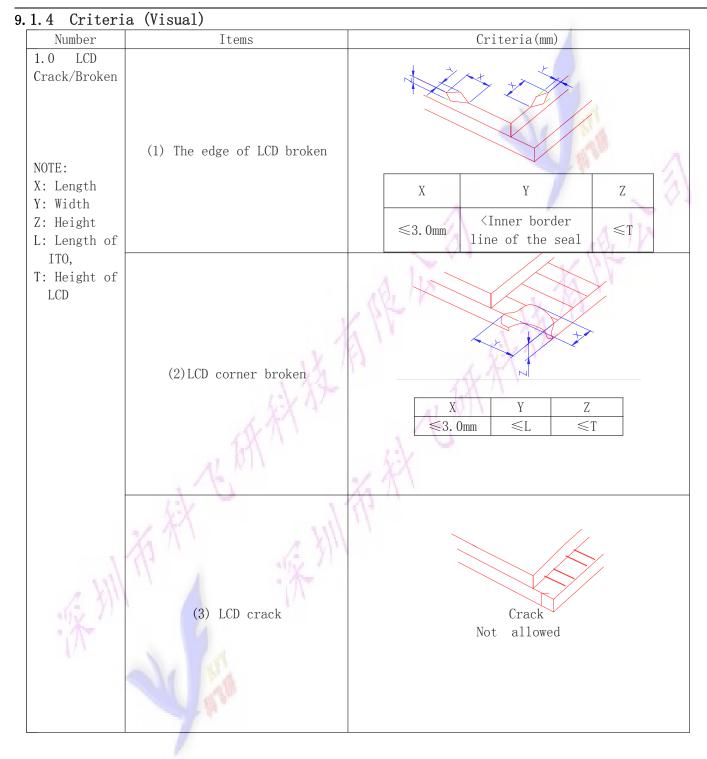
According to GB/T 2828-2003 ; , normal inspection, Class II AQL:

Major defect	Minor defect
0.65	1.5

LCD: Liquid Crystal Display , TP: Touch Panel , LCM: Liquid Crystal Module

No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	 No display, Open or miss line Display abnormally, Short Backlight no lighting, abnormal lighting. TP no function 	Major
2	Missing Missing component		
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	7. 3
4	Color tone	Color unevenness, refer to limited sample	
5	Soldering appearance	Good soldering , Peeling off is not allowed.	Minor
6	LCD/Polarizer/TP	Black/White spot/line, scratch, crack, etc.	VYV

_



Number Items Criteria (mm)

COG-T350KFY-05

						00	
2.0	Spot defect	① light dot (LCD/ dent, stain)	TP/Polarizer	black/wh	ite spo	ot , light	dot, pinhole,
		Zone	Ac	ceptable	Qty		
		Size (mm)	А	В		С	S.
	X	Φ ≤0.10	Ignoi	re		-	and a
	$\Phi = (X+Y)/2$	$0.10 < \Phi \le 0.15$	3(distance	$e \ge 10$ mm)		Ignore	* 2
	- (11 1)/2	$0.15 < \Phi \le 0.2$	1	1		1 gliot e	~ ~ ~ 1
		0. 2<Φ	0	1	1		A. M.
		②Dim spot (LCD/TP	/Polarizer di	m dot, l	ight le	akage, dar	k spot)
		Zone	110	ceptable			
		Size (mm)	A	В		c -	
		Φ≤0.1	Ignoi	ce	XX	Xa	
		0.1<Φ≤0.2	2(distance	$e \ge 10$ mm)	X ^	T	
		0. 2< Φ ≤ 0. 3	1	A	V.L.	Ignore	
		Φ>0.3	0	X			
		③ Polarizer accid	ented spot				
	22 N	Zone	Ad	cceptable	e Qty		
	AT III	Size (mm)	А	В		С	
		Φ≤0.2	Igno	re			
-112	21.1	0. 2< Φ ≤0. 5	2(distanc	$e \ge 10$ mm)		Ignore	
1		Φ>0.5	0				
Line defect (LCD/TP					. 11	0.	
	/Polarizer	Width (mm)	Length(mm)		eptable	1	
	black/white line,	± ≤0.00	т	A	В	С	
	scratch, stain)	Φ≤0.03	Ignore	Igno		_	
		0.03<₩≤0.05	L≤3.0	N≤		Ignore	
		0.05<₩≤0.08	L≤2.0	N≤			
		0.08 <w< td=""><td>Defi</td><td>ne as spo</td><td>ot defe</td><td>ct</td><td></td></w<>	Defi	ne as spo	ot defe	ct	
L	1	1					

Acceptable Qty Zone Size (mm) А В С Polarizer Ф≤0.2 Ignore Bubble 3.0 $2 \text{(distance} \ge 10 \text{mm})$ 0. $2 \le \Phi \le 0.4$ Ignore 0.4<Φ≤0.6 1 0.6<Φ 0 According to IPC-A-610C class II standard . Function defect and missing 4.0 SMT part are major defect , the others are minor defect. Acceptable Qty Size Φ (mm) TP bubble/ С А В accidented Ф≤0.1 Ignore spot 0.1<Φ≤0.2 2 Ignore 0.2<Φ≤0.3 1 0.3<Φ 0 Assembly beyond the edge of backlight ≤ 0.15 mm deflection 5.0] 規律性 TF Related Newton Ring area>1/3 TP area NG Newton Ring Newton Ring area $\leq 1/3$ TP area OK 2.挑肆性 似牛顿环

Ver.01

COG-T350KFY-05

		TP corner		Х	Y	Z		v X X
		broken X: length Y: width		X≤3.0mm	Y≤3.0mm	Z <lc thickn</lc 		
		Z: height		Circuitry 11owed.	broken is	not		
		TP edge			1			A 100 A
		broken		Х	Y	Ζ		X
		X: length Y: width		X≤6.0mm	Y≤2.0mm	Z <lci thickne</lci 		Z Z
		Z: height			y broken is	s not		
			a.	llowed.	~			0.42
<u>Cr</u>	<u>iteria (functio</u>	<u>onal items)</u>						VYV
	Num	ber		Ι	tems	2		Criteria (mm)
	:	1		No d	lisplay			Not allowed
		2		Missin	g segment			Not allowed
	3			Short		13	Not allowed	
		4	I	Backlight	no lightin	ıg		Not allowed
		5			function	A	Y N	Not allowed

9.2 RELIABILITY TEST

NO	ITEM	CONDTTION	STANDARD					
1	High Temp. Storage	80°C, 240 hours	1. Functional test					
2	Low Temp. Storage	-30°C, 240 hours	is OK. Missing Segment,					
3	High Temp. Operation	70°C, 240 hours	short, unclear segment, non-					
4	Low Temp. Operation	-20°C, 240 hours	display, display					
5	High temperature and high Humidity storage	40°C,90%RH ,240 hours	abnormally and liquid crystal leak are un-allowed. 2. No low					
6	Thermal and cold shock	Static state, -20℃ (30 Min) ~70℃ (30 Min) ~ -20℃ (30Min), packaging, 10 cycles	temperature bubbles, end seal loose and fall, frame rainbow.					
7	Vibration test	Packaging, Frequency : 10-55Hz Amplitude : 1.0mm, Each direction on X,Y axe 0.5 houre, circle 2 hours	 Function test is OK. No glass crack, chipped glass, end seal loose and fall. 					

	8	Dropping test	Pack products into the carton box. Drop it from 80cm height to ground. Once for each side of the carton		
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NOTE:

9.2.1 The reliability items will be fully performed in new sample qualification,

9.2.2 The reliability status will be tested as monitor during mass production. Individual reliability test shall be

performed by lot , Moreover, the individual reliability item shall be decided according to reliability plan.

9.2.3 All samples are inspected after keeping in the room with normal temperature and humidity for 2 hours or above.

9.2.4 Vibration test: It is not necessary to test for those products without assembly frame , back light , PCB and so on.

9.2.5 Dropping test : It is necessary for affirming new package.

 $9.\,2.\,6$ For the high temperature and high humidity test, pure water of over 10 MQ.cm should be used.

9.2.7 Each test item applies for test LCM only once .Then tested LCM cannot be used again in any other test item.

9.2.8 The quantity of LCM examination for each test item is 5pcs to 10pcs.

9.3 Safetv instructions

9.3.1 If the LCD panel breaks, be careful not to get any liquid crystal substance in your mouth.

9.3.2 If the liquid crystal substance touches your skin or clothes, please wash it off immediately by using soap and water.

9.4 Handling Precautions

9.4.1 Avoid static electricity damaging the LSI.

9.4.2 Do not remove the panel or frame from the module .

9.4.3 The polarizing plate of the display is very fragile . So, please handle it very

carefully.

9.4.4 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of the plate.

9.4.5 The color tone of display and background of LCM has the possibility to be changed in the storage temperature range.

9.4.6 Pay attention to the working environment, as the element may be destroyed by static electricity.

-Be sure to ground human body and electric appliance during work.

--Avoid working in a dry environment to minimize the generations of static electricity.

--Static electricity may be generated when the protective film is fast peeled off.

9.4.7 When soldering the terminal of LCM, make certain the AC power source of soldering iron does not leak.

10.4.8 If the display surface becomes contaminated ,breathe on the surface and gently wipe it with a soft-dry- clean cloth .If it is heavily contaminated ,moisten cloth with the following solvent(ex:Ethyl alcohol).Solvents other than those above-mentioned may damage the polarizer(Especially ,do not use them .ex: Warter / Ketone)

9.5 Operation instructions

Ver.01

- 9.5.1 It is recommended to drive the LCD within the specified voltage limits, try to adjust the operating voltage for the optimal contrast, the color and contrast of LCD panel will varies at different temperature.
- 9.5.2 Response time is greatly delayed at low operating temperature range. However, this does not mean the LCD will be out of the order, It will recover when it returns to the specified temperature range.
- 9.5.3 If the display area is pushed hard during operation, the display will become abnormal.
- 9.5.4 Do not operate the LCD at the environments over the specified conditions, this may cause damage on the LCD and shorten the lifetime.

9.6 Storage instructions:

- 10.6.1 Store LCDs in a sealed polyethylene bag.
- 10.6.2 Store LCDs in a dark place, Do not expose to sunlight or fluorescent light. Keep the temperature between 0°C and 35°C.
- 10.6.3 Avoid the polarizer touch any other object, (It is recommended
- to store them in the container in which they were shipped.)

9.7 Limited Warranty

- 9.7.1 will replace or repair any of its LCD modules, which are found to be defective, when inspected in accordance with LCM acceptance standards (copies available upon request) for a period of 12 months from ink- print date on product
- 9.7.2 Any defects must be returned to within 60 days since ship-out. Confirmation of such date shall be based on freight documents. The warranty liability of wasam limited to repair and/or replacement on defects above (7.1,7.2)
- 9.7.3 No warranty can be granted if the precautions stated above have been disregarded. The typical samples are as below:
 - -- LCD glass crack/break
 - --PCB outlet is damaged or modified.
 - --PCB conductors damaged.
 - --Circuit modified with by grinding, engraving or painting varnish.
 - --FPC crack

9.7.4 Modules must be returned with sufficient description of the failures of defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB outlet, conductors and terminals. Modules must be packed with the container in which they were shipped.



- 10. Packing method
 - -----TBD