COG240160C

LCD Module User Manual

SHENZHEN KEFEIYAN TECHNOLOGY CO., LTD.

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1. Basic Specifications

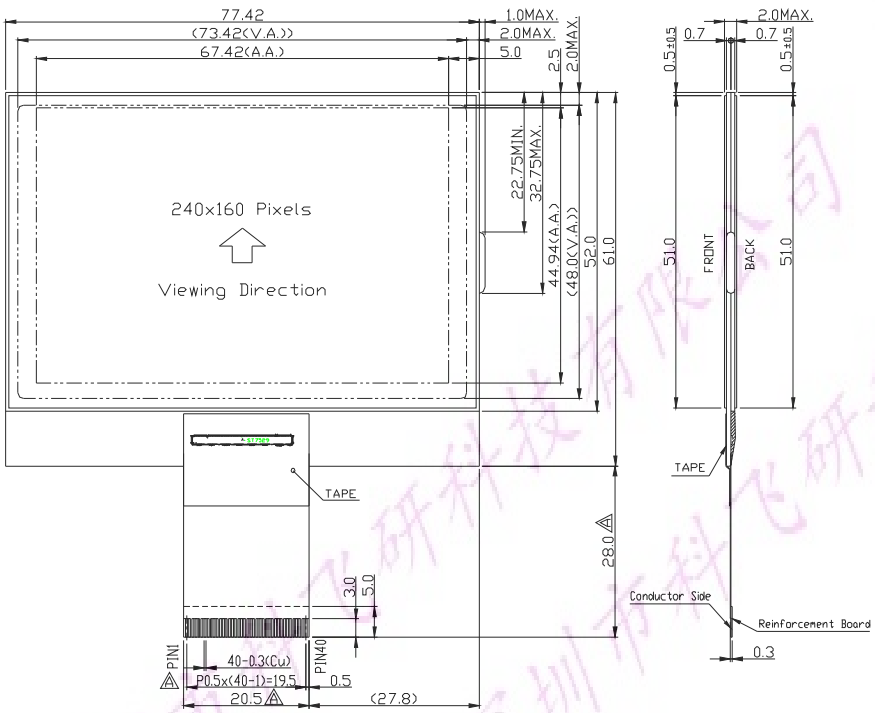
1.1 Display Specifications

1>LCD Display Mode : FSTN-Gray, Positive, Transflective
2>Viewing Angle : 6H
3>Driving Method : 1/160 Duty, 1/12 Bias
4 >Backlight : White

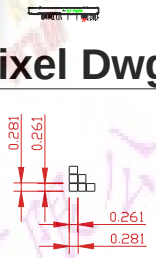
1.2 Mechanical Specifications

1>Outline Dimension : 77.42 x61.0 x 2.0mm (See attached Outline Drawing for Deta

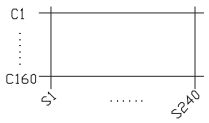
Main Dwg



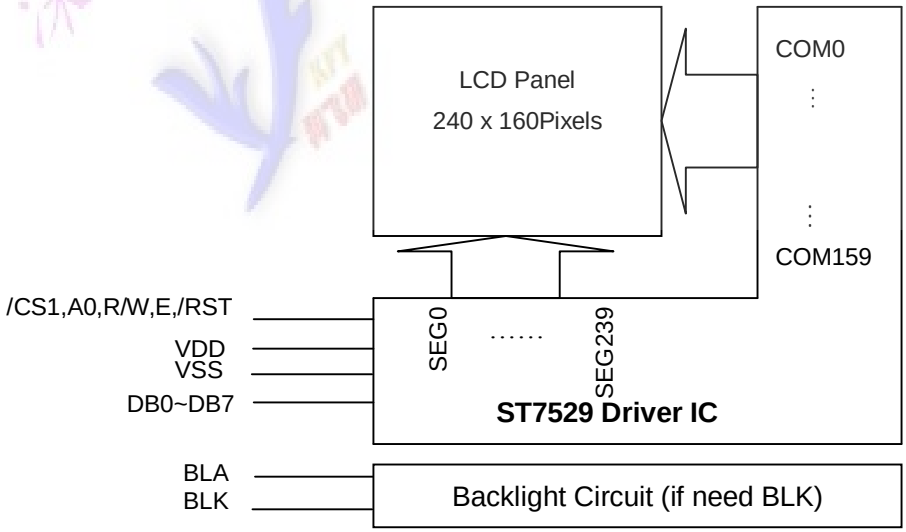
Pixel Dwg



Wiring Dwg



1.3 Circuit Diagram



1.4 Terminal Function

Pin No.	Pin Name	Function
1	A0	Data/Command control.
2	R/W	Write (/WR) control signal input.
3~10	DB0~DB7	Low 8-bit Date bus
11~18	DB8~DB15	High 8-bit Date bus
19	/RD	Read (/RD) control signal input.
20	/RST	Reset Signal
21	IF3	8bit(L) OR 16bit(H) selection input,8080
22	XCS	chip selection input
23	VDD	Power supply for logic circuit 3.3V
24	VSS	Negative power supply,0V
25	VDD2	Power supply for Booster Circuit
26	C7P	Caputure of Booster Circuit
27	C5P	Caputure of Booster Circuit
28	C3P	Caputure of Booster Circuit
29	C1N	Caputure of Booster Circuit
30	C1P	Caputure of Booster Circuit
31	C2P	Caputure of Booster Circuit
32	C2N	Caputure of Booster Circuit
33	C4P	Caputure of Booster Circuit
34	C6P	Caputure of Booster Circuit
35	VLCD	An external LCD supply voltage can be supplied using the VLCD
36	V4	LCD driver supply voltages
37	V3	LCD driver supply voltages
38	V2	LCD driver supply voltages
39	V1	LCD driver supply voltages
40	V0	LCD driver supply voltages

2. Absolute Maximum Ratings

Items	Symbol	MIN.	MAX.	Unit	Condition
Supply Voltage	V_{DD}	-0.3	+3.3	V	Vss = 0V
Input Voltage	V_{IN}	-0.3	V_{DD}+0.3	V	Vss = 0V
Operating Temperature	Top	-10	+60	℃	No Condensation
Storage Temperature	Tst	-20	+70	℃	No Condensation

3. Electrical Characteristics

3.1 DC Characteristics

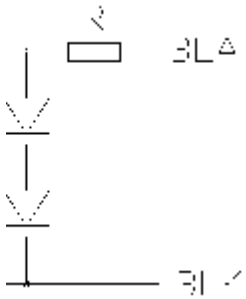
Vss = 0V,Top = 25℃

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Operating Voltage	V_{DD}	2.4	-	3.3	V	VDD
Input High Voltage	V_{IH}	0.7 x V_{DD}	-	V_{DD}	V	/CS1,/RES,A0,/WR, E,D0~D7
Input Low Voltage	V_{IL}	Vss	-	0.3 x V_{DD}	V	
Output High Voltage	V_{OH}	0.7 x V_{DD}	-	V_{DD}	V	D0~D7
Output Low Voltage	V_{OL}	Vss	-	0.3 x V_{DD}	V	D0~D7
Input Leakage Current	I_{LI}	-1.0	-	1.0	μA	VDD
Output Leakage Current	I_{LO}	-	-	-	μA	VDD

3.2 LED Backlight Circuit (if need BLK)

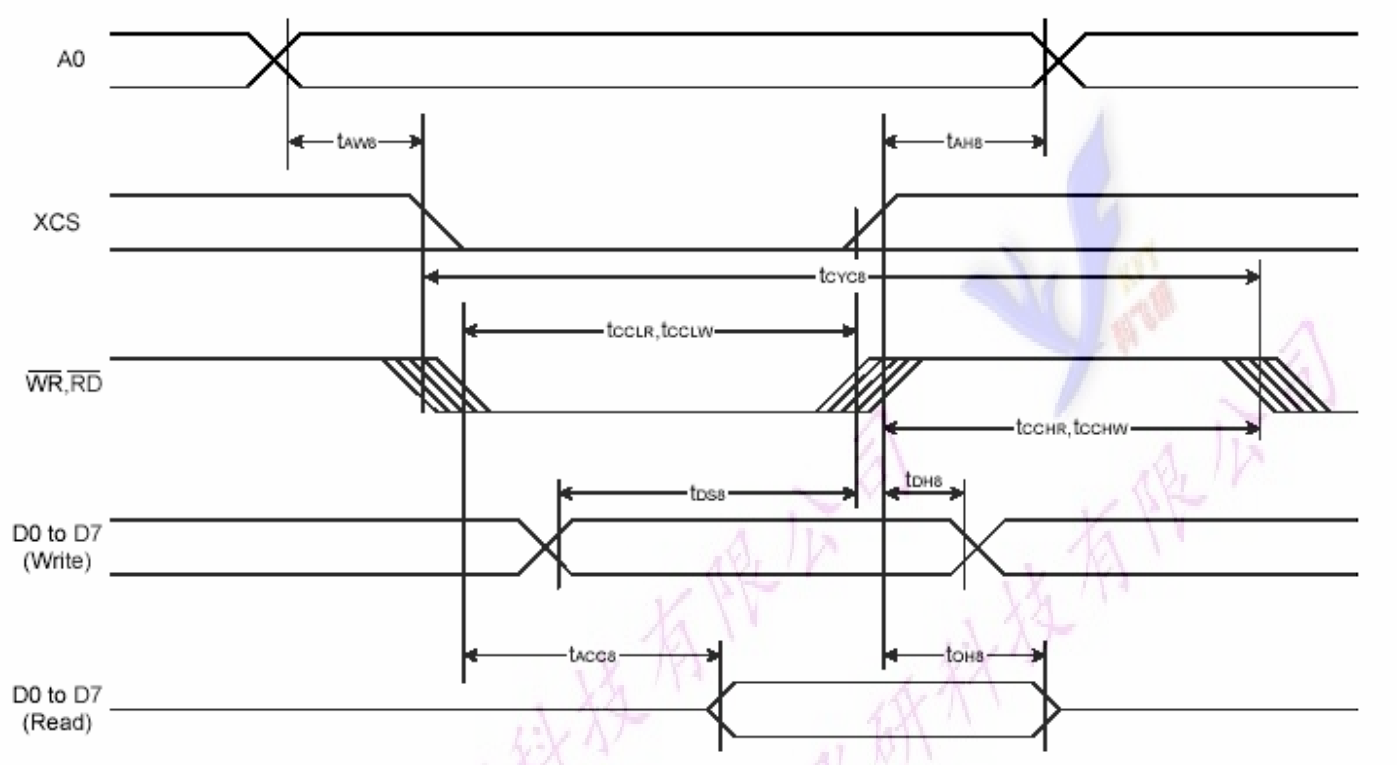
Vss = 0V,Top = 25℃

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Forword Voltage	Vf BLA	-	3.3	-	V	VDD
Forword Current	If BLA	-	-	10	mA	VDD



3.3 AC Characteristics

3.3.1 8080 Mode System Bus Timing



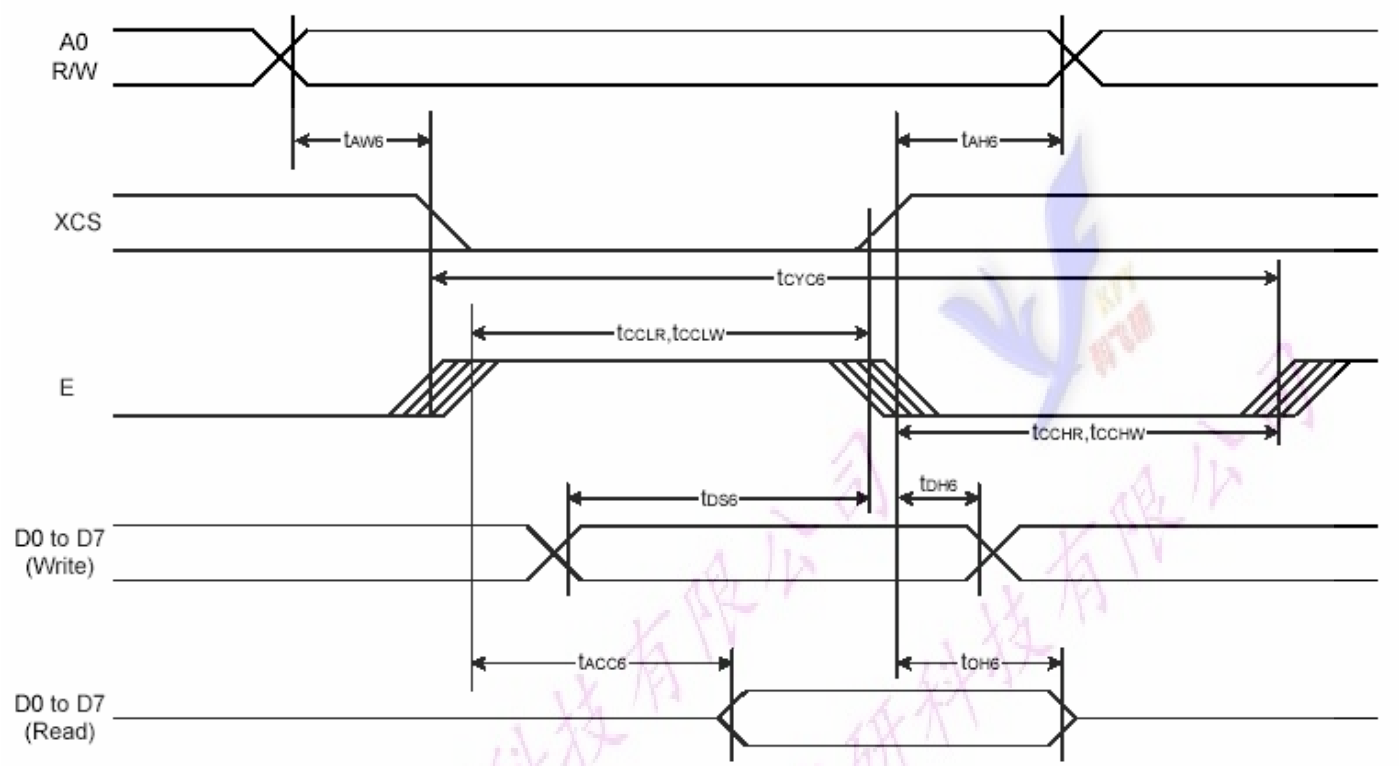
Vdd = 3.3V, Ta = -40-85℃

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
System cycle time	Tcyc8	200	-	-	ns	-
Address setup time(A0)	Taw8	20	-	-	ns	-
Address hold time(A0)	Tah8	20	-	-	ns	-
Control Low Pulse wide(/RD)	tcclr	100	-	-	ns	-
Control Low Pulse wide(/WR)	tcclw	100	-	-	ns	-
Control High Pulse wide(/RD)	tcchr	100	-	-	ns	-
Control High Pulse wide(/WR)	tcchw	100	-	-	ns	-
Data steup time	Tds8	150	-	-	ns	-
Data hold time	Tdh8	20	-	-	ns	-
/RD access time(*a)	Tacc8	-	-	40	ns	-
Output disable time(*a)	Tch8	15	-	30	ns	-

Note:

***a. all timing is using 20％ and 80％ of VDD as the reference.**

3.3.2 6800 Mode System Bus Timing



Vdd = 3.3V,Ta = -40-85℃

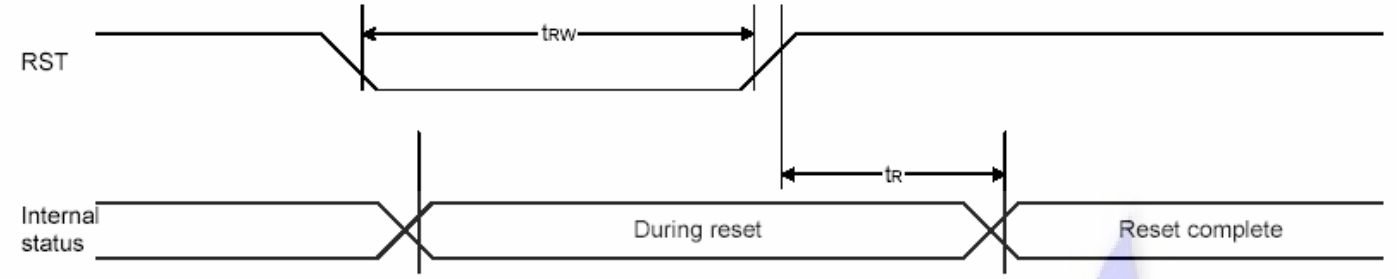
Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
System cycle time	Tcyc6	200	-	-	ns	-
Address setup time(A0)	Taw6	20	-	-	ns	-
Address hold time(A0)	Tah6	20	-	-	ns	-
Control Low Pulse wide(/RD)	tcclr	100	-	-	ns	-
Control Low Pulse wide(/WR)	tcclw	100	-	-	ns	-
Control High Pulse wide(/RD)	tcchr	100	-	-	ns	-
Control High Pulse wide(/WR)	tcchw	100	-	-	ns	-
Data steup time	Tds6	150	-	-	ns	-
Data hold time	Tdh6	20	-	-	ns	-
/RD access time(*a)	Tacc6	-	-	40	ns	-
Output disable time(*a)	Tch6	-	-	30	ns	-

Note:

***a. all timing is using 20％ and 80％ of VDD as the reference.**

***b. CL = 100pF**

3.4 Reset Timing



(VDD=3.3V,Ta=-40-85℃)

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Reset time	Tr	-	-	1	μS	-
Reset Low pules width	Trw	1	-	-	μS	-

(VDD=2.7V,Ta=-40-85℃)

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Reset time	Tr	-	-	1.5	μS	-
Reset Low pules width	Trw	1.5	-	-	μS	-

Note:

***a. all timing is using 20％ and 80％ of VDD as the reference.**

4. Function specifications

4.1 The Parallel Interface

Shared	6800 Mode		8080 Mode		Function
A0	R/W	E	/RW	H	
H	H	H	L	H	Reads the display data
H	L	H à L	H	L à H	Writes the display data
L	H	H	L	H	Staus read
L	L	H à L	H	L à H	Write Command data

4.2 Basic Setting

To drive the LCD module correctly and provide normally display, please use the following seting

1> ADC = 0 (normal)
2> SHL select = 1(reverse)
3> LCD Bias Select = 1/12
4> Initial Display Line = 0
5> Entire Display ON/OF = OFF(normal)
6> Reverse Display ON/OF = OFF(normal)
7> Set Power Control Set:
Voltage follower = ON,voltage converter = ON,Voltage regulator = ON
8> Display ON/OF =ON

4.3 Resetting the LCD module

The LCD module should be initialized bu using /RES terminal.
While turning on the VDD and VSS power supply, maintain /RES terminal at LOW level, After the Power supply stabilized, release the reset terminal(/RES = High)

4.4 Display Commands

Ext=0 or Ext=1

Index	Command	A0	RD	WR	D7	D6	D5	D4	D3	D2	D1	D0	Function	Hex	Parameter
1	Ext In	0	1	0	0	0	1	1	0	0	0	0	Ext=0 Set	30	None
2	Ext Out	0	1	0	0	0	1	1	0	0	0	1	Ext=1 Set	31	None

Ext=0

Index	Command	A0	RD	WR	D7	D6	D5	D4	D3	D2	D1	D0	Function	Hex	Parameter
1	DISON	0	1	0	1	0	1	0	1	1	1	1	Display On	AF	None
2	DISOFF	0	1	0	1	0	1	0	1	1	1	0	Display Off	AE	None
3	DISNOR	0	1	0	1	0	1	0	0	1	1	0	Normal Display	A6	None
4	DISINV	0	1	0	1	0	1	0	0	1	1	1	Inverse Display	A7	None
5	COMSCN	0	1	0	1	0	1	1	1	0	1	1	COM Scan Direction	BB	1 byte
6	DISCTRL	0	1	0	1	1	0	0	1	0	1	0	Display Control	CA	3 bytes
7	SLPIN	0	1	0	1	0	0	1	0	1	0	1	Sleep In	95	None
8	SLPOUT	0	1	0	1	0	0	1	0	1	0	0	Sleep Out	94	None
9	LASET	0	1	0	0	1	1	1	0	1	0	1	Line Address Set	75	2 bytes
10	CASET	0	1	0	0	0	0	1	0	1	0	1	Column Address Set	15	2 bytes
11	DATSDR	0	1	0	1	0	1	1	1	1	0	0	Data Scan Direction	BC	3 bytes
12	RAMWR	0	1	0	0	1	0	1	1	1	0	0	Writing to Memory	5C	Data
13	RAMRD	0	1	0	0	1	0	1	1	1	0	1	Reading from Memory	5D	Data
14	PTLIN	0	1	0	1	0	1	0	1	0	0	0	Partial display in	A8	2 bytes
15	PTLOUT	0	1	0	1	0	1	0	1	0	0	1	Partial display out	A9	None
16	RMWIN	0	1	0	1	1	1	0	0	0	0	0	Read and Modify Write	E0	None
17	RMWOUT	0	1	0	1	1	1	0	1	1	1	0	RMW end	EE	None
18	ASCSET	0	1	0	1	0	1	0	1	0	1	0	Area Scroll Set	AA	4 bytes
19	SCSTART	0	1	0	1	0	1	0	1	0	1	1	Scroll Start Set	AB	1 byte
20	OSCON	0	1	0	1	1	0	1	0	0	0	1	Internal OSC on	D1	None
21	OSCOFF	0	1	0	1	1	0	1	0	0	1	0	Internal OSC off	D2	None
22	PWRCTRL	0	1	0	0	0	1	0	0	0	0	0	Power Control	20	1 byte
23	VOLCTRL	0	1	0	1	0	0	0	0	0	0	1	EC control	81	2 bytes
24	VOLUP	0	1	0	1	1	0	1	0	1	1	0	EC increase 1	D6	None
25	VOLDOWN	0	1	0	1	1	0	1	0	1	1	1	EC decrease 1	D7	None
26	RESERVED	0	1	0	1	0	0	0	0	0	1	0	Not Use	82	0
27	EPSRRD1	0	1	0	0	1	1	1	1	1	0	0	READ Register1	7C	None

28	EPSRRD2	0	1	0	0	1	1	1	1	1	0	1	READ Register2	7D	None
29	NOP	0	1	0	0	0	1	0	0	1	0	1	NOP Instruction	25	None
30	STREAD	0	0	1	Read Data								Status Read		

Ext=1

Index	*Command*	*A0*	*RD*	*WR*	*D7*	*D6*	*D5*	*D4*	*D3*	*D2*	*D1*	*D0*	*Function*	*Hex*	*Parameter*
1	Gray 1 Set	0	1	0	0	0	1	0	0	0	0	0	FRAME 1 Gray PWM Set	20	16 bytes
2	Gray 2 Set	0	1	0	0	0	1	0	0	0	0	1	FRAME 2 Gray PWM Set	21	16 bytes
3	Wt. Set	0	1	0	0	0	1	0	0	0	1	0	Weight Set	22	3 bytes
4	ANASET	0	1	0	0	0	1	1	0	0	1	0	Analog Circuit Set	32	3 bytes
5	DITHOFF	0	1	0	0	0	1	1	0	1	0	0	Dithering Circuit Off	34	None
6	DITHON	0	1	0	0	0	1	1	0	1	0	1	Dithering Circuit On	35	None
7	EPCTIN	0	1	0	1	1	0	0	1	1	0	1	Control EEPROM	CD	1 byte
8	EPCOUT	0	1	0	1	1	0	0	1	1	0	0	Cancel EEPROM	CC	None
9	EPMWR	0	1	0	1	1	1	1	1	1	0	0	Write to EEPROM	FC	None
10	EPMRD	0	1	0	1	1	1	1	1	1	0	1	Read from EEPROM	FD	None

Note:

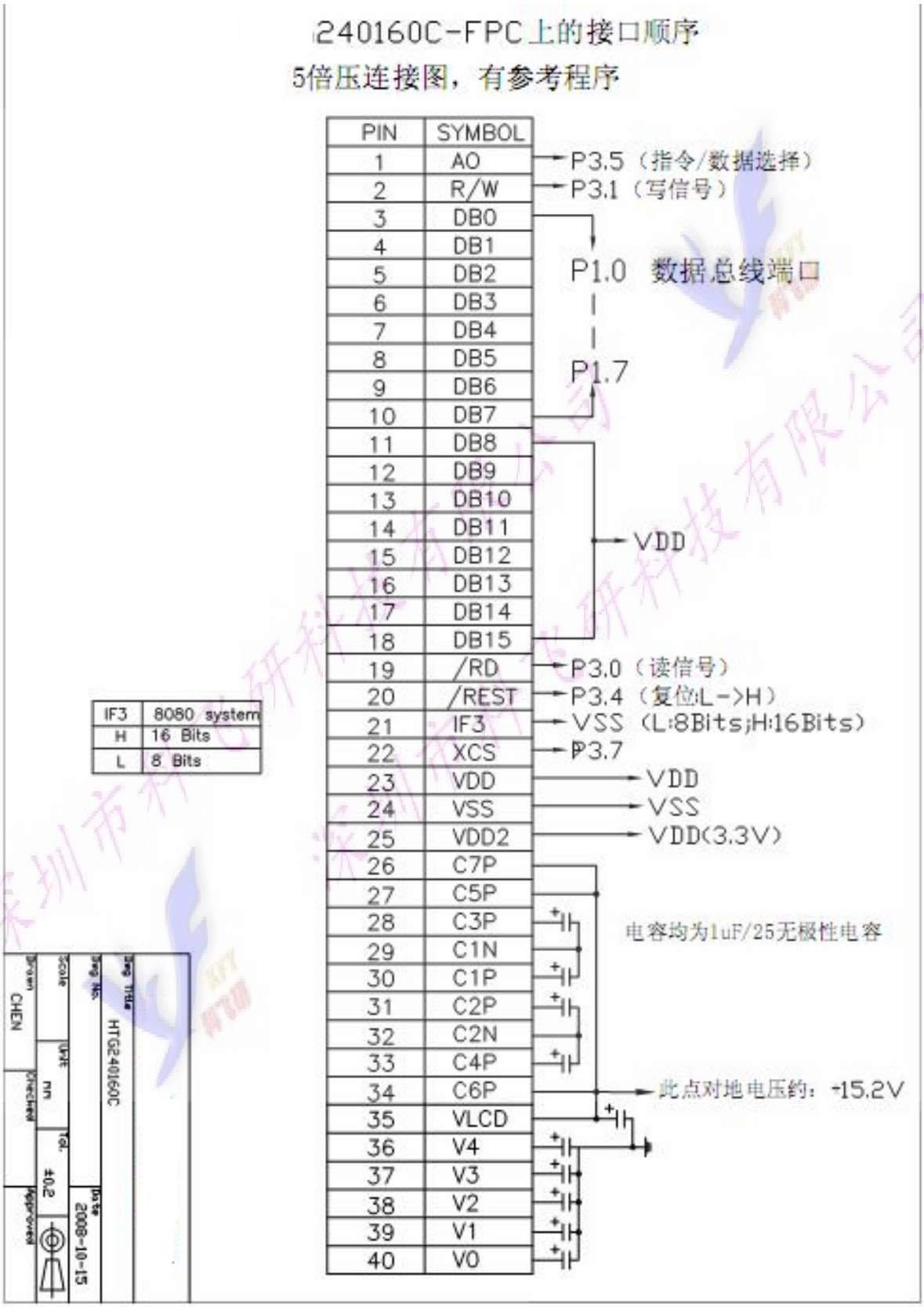
***a. For the details of the Display Commands, please refer to ST7529 data sheet**

4.5 Basic Operating Sequence

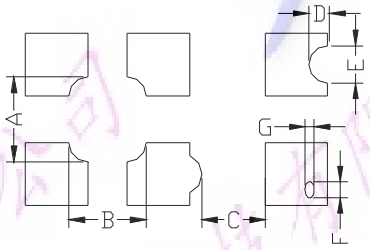
4.5.1 Initialization Sequence

	Code Function										Note
	A0	D7	D6	D5	D4	D3	D2	D1	D0	hex	
Turn on Power Supply VDD & VSS While maintaining /RES at LOW	-	-	-	-	-	-	-	-	-	-	-
Wait until power supply is stabilized	-	-	-	-	-	-	-	-	-	-	-
Release the /RES Reset Signal (/RES = High)	-	-	-	-	-	-	-	-	-	-	
EXT=Use the “ext=0” Command	0	1	0	1	0	0	0	1	1	30H	EXT=0
Sleep Out	0	1	0	0	1	0	1	0	0	94H	Sleep Out
Osc on	0	1	1	0	1	0	0	1	0	D2H	
Com scan dtrec	0	0	1	0	0	0	0	0	0	BBH	
0-65/160-66	1	0	0	0	0	0	0	0	1	01H	
Display Control	0	1	1	0	0	1	0	1	0	CAH	
CL Dividin ratio=0	1	0	0	0	0	0	1	0	1	04H	
Duty=1/12	1	0	0	0	0	0	0	0	0	27H	
FR inverse-set value	1	0	1	0	0	0	1	1	0	00H	
EXT=Use the “ext=0” Command	0	0	0	1	1	0	0	0	1	31H	
Analogl	0	0	0	0	1	0	0	0	0	32H	
Osc freq	1	0	0	0	0	0	0	1	0	03H	
Booster effic	1	0	0	0	0	0	0	1	1	00H	
Bise	1	0	0	0	0	0	0	1	0	02H	
EXT=Use the “ext=0” Command	0	0	0	1	1	0	0	0	1	30H	
EC contral	0	1	0	0	0	0	0	1	1	81H	
Vop[5:0]	1	0	0	1	1	0	0	1	1	30H	
Vop[8:6]	1	0	0	0	0	0	0	0	0	03H	
Power contral	0	0	0	0	1	0	0	0	0	20H	
D0 = regulator / D1 = follwer / D3 = booster	1	0	0	0	0	1	0	1	1	0BH	
Close auto sampling	0	1	0	1	0	1	1	1	1	60H	
inverse display	0	1	0	1	0	0	1	1	1	A7H	
data scan direc.	0	1	0	1	1	1	1	0	0	BCH	
C/L com/line,ci,li:address scan direc.	1	0	0	0	0	0	0	0	0	00H	
-------	1	0	0	0	0	0	0	0	0	00H	
Gray scale	1	0	0	0	0	1	0	0	1	02H	
Exite the partial display mode	0	0	1	0	1	1	0	1	1	A9H	
display on	0	0	1	0	1	1	1	1	0	AFH	
EXT-0	0	0	0	1	1	0	0	0	0	30H	
Column address set	0	0	0	0	1	0	1	0	1	15H	
PB1	1	X	X	X	X	X	X	X	X	PB1	Start column
PB2	1	X	X	X	X	X	X	X	X	PB2	End coulmn
Line address set	0	0	1	1	1	0	1	0	1	75H	
PB1	1	X	X	X	X	X	X	X	X	PB1	Start line
PB2	1	X	X	X	X	X	X	X	X	PB2	End line
Data	1										8 bit data

4.5.2 8080serial 8-bit Inteface



5. Inspection Standards

Item	Criterion for defects	Defect type
1) Display on inspection	(1) Non display (2) Vertical line is deficient (3) Horizontal line is deficient (4) Cross line is deficient	Major
2) Black / White spot	Size Φ(mm) Acceptable number Φ≤0.3 Ignore (note) 0.3<Φ≤0.45 3 0.45<Φ≤0.6 1 0.6<Φ 0	Minor
3) Black / White line	Length (mm) Width (mm) Acceptable number L≤10 W≤0.03 Ignore 5.0≤L≤10 0.03<W≤0.04 3 5.0≤L≤10 0.04<W≤0.05 2 1.0≤L≤10 0.05<W≤0.06 2 1.0≤L≤10 0.06<W≤0.08 1 L≤10 0.08<W follows 2) point defect Defects separate with each other at an interval of more than 20mm	Minor
4) Display pattern	 $\frac{A+B}{2}\leq 0.28$ $0<C$ $\frac{D+E}{2}\leq 0.25$ $\frac{F+G}{2}\leq 0.25$ Note: 1) Up to 3 damages acceptable 2) Not allowed if there are two or more pinholes every three-fourth inch.	Minor
5) Spot-like contrast irregularity	Size Φ(mm) Acceptable Number Φ≤0.7 Ignore (note) 0.7<Φ≤1.0 3 1.0<Φ≤1.5 1 1.5<Φ 0 Note: 1) Conformed to limit samples. 2) Intervals of defects are more than 30mm.	Minor
6) Bubbles in polarizer	Size Φ(mm) Acceptable Number Φ≤0.4 Ignore (note) 0.4<Φ≤0.65 2 0.65<Φ≤1.2 1 1.2<Φ 0	Minor
7) Scratches and dent on the polarizer	Scratches and dent on the polarizer shall be in the accordance with "2) Black/white spot", and "3) Black/White line".	Minor
8) Stains on the surface of LCD panel	Stains which cannot be removed even when wiped lightly with a soft cloth or similar cleaning.	Minor
9) Rainbow color	No rainbow color is allowed in the optimum contrast on state within the active area.	Minor
10) Viewing area encroachment	Polarizer edge or line is visible in the opening viewing area due to polarizer shortness or sealing line.	Minor
11) Bezel appearance	Rust and deep damages that are visible in the bezel are rejected.	Minor
12) Defect of land surface contact	Evident crevices that are visible are rejected.	Minor
13) Parts mounting	(1) Failure to mount parts (2) Parts not in the specifications are mounted (3) For example: Polarity is reversed, HSC or TCP falls off.	Minor
14) Part alignment	(1) LSI, IC lead width is more than 50% beyond pad outline. (2) More than 50% of LSI, IC leads is off the pad outline.	Minor
15) Conductive foreign matter (solder ball, solder hips)	(1) 0.45<Φ, N≥1 (2) 0.3<Φ≤0.45, N≥1, Φ: Average diameter of solder ball (unit: mm) (3) 0.5<L, N≥1, L: Average length of solder chip (unit: mm)	Minor
16) Bezel flaw	Bezel claw missing or not bent	Minor
17) Indication on name plate (sampling indication label)	(1) Failure to stamp or label error, or not legible.(all acceptable if legible) (2) The separation is more than 1/3 for indication discoloration, in which the characters can be checked.	Minor

6. Handling Precautions

6.1 Mounting method

A panel of LCD module made by our company consists of two thin glass plates with polarizers that easily get damaged.
And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board (PCB), extreme care should be used when handling the LCD modules.

6.2 Cautions of LCD handling and cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.
-Isopropyl alcohol
-Ethyl alcohol
-Trichlorotriflorothane
Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.
Do not use the following solvent:
-Water
-Ketene
-Aromatics

6.3 Caution against static charge

The LCD module use C-MOS LSI drivers. So we recommend you:
Connect any unused input terminal to V_{dd} or V_{ss}. Do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

6.4 Packaging

-Module employs LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
-To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity.

6.5 Caution for operation

-It is an indispensable condition to drive LCD module within the limits of the specified voltage since the higher voltage over the limits may cause the shorter life of LCD module.
-An electrochemical reaction due to DC (direct current) causes LCD undesirable deterioration so that the uses of DC (direct current) drive should be avoided.
-Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD module may show dark color in them. However those phenomena do not mean malfunction or out of order of LCD module, which will come back in the specified operating temperature.

6.6 Storage

In the case of storing for a long period of time, the following ways are recommended:
-Storage in polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with not desiccant.
-Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping the storage temperature range.
-Storing with no touch on polarizer surface by any thing else.

6.7 Safety

-It is recommendable to crash damaged or unnecessary LCD into pieces and to wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
-When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well at once with soap and water.